

JP095 U.S. PRO
09/635124



Class	Subclass
438	114
I/F CLASSIFICATION	

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
MS SCANNED MS③	11/6/01

APPLICATION NO. 09/635124	CONT/PRIOR D F	CLASS 438	SUBCLASS 12.7	ART UNIT 2812	EXAMINER GR1122
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Applicants:
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Title: Method and mold for manufacturing semiconductor device, semiconductor device and method for mounting the device.

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
438	114	438	126	127	462	465	
INTERNATIONAL CLASSIFICATION		438	126	127	462	465	
401L	21/44	264	272.17				
401L	21/48						
401L	21/50						
461L	21/301						
401L	21/46						

☐ Continued on Issue Slip inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg. 131	Figs. Drwg. 323	Print Fig. 1,4,7A,7B,8	Total Claims 5253	Print Claim for C.G. 1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____	(Assistant Examiner) _____ (Date) _____ DAVID E. GRAYBILL Primary Examiner (Date) 10-22-01			NOTICE OF ALLOWANCE MAILED 11/6/01	
	(Legal Instruments Examiner) _____ (Date) _____			ISSUE FEE Amount Due 1280 Date Paid _____	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER 044	
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Formal drawings _____ sheets

(FACE)